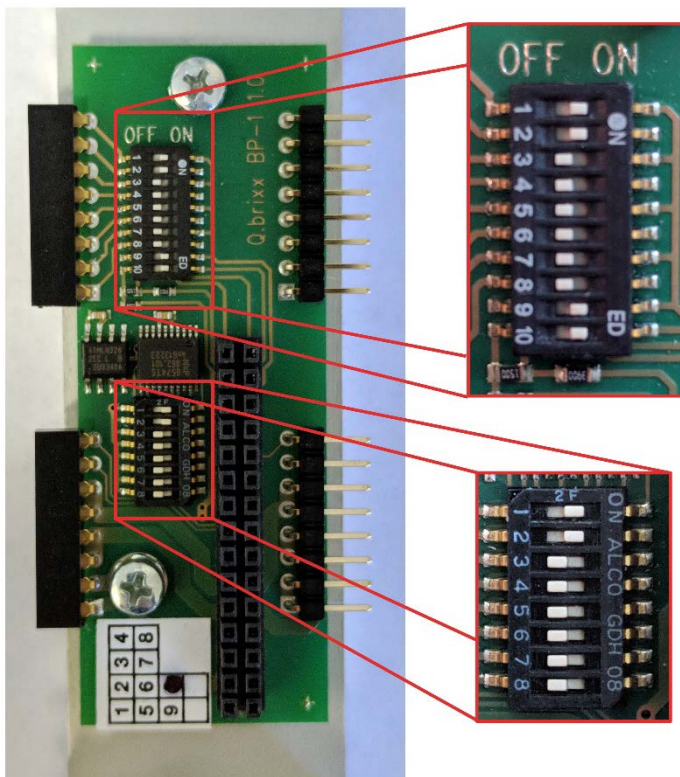


The location of the DIP switches on a Q.brixx base has been re-designed because of the bus lines. To improve functionality and to reduce disturbances that could potentially occur at high baud rates, the DIP switches for the baud rate setting are placed closer to the physical location of the bus lines.

## New Base Configuration (Q.brixx2)



ON the 10-pin DIP switch block, the UARTs can be set and bus termination activated:

UART1: Pins 1 and 2 UP (Pins 3-8 DOWN)

UART2: Pins 3 and 4 UP (Pins 1-2, 5-8 DOWN)

UART3: Pins 5 and 6 UP (Pins 1-4, 7-8 DOWN)

UART4: Pins 7 and 8 UP (Pins 1-6 DOWN)

Bus Termination ON: Pins 9 and 10 UP

Bus Termination OFF: Pins 9 and 10 DOWN

ON the 8-pin DIP switch block, the address of the modules is set using binary:

*Example:*

Address 1: Pin 1 UP (Pins 2-8 DOWN)

Address 2: Pin 2 UP (Pins 1, 3-8 DOWN)

Address 3: Pins 1 and 2 UP (Pins 3-8 DOWN)

If the address is not set using the DIP switches, the address must be set using test.commander.

## Old Base Configuration 1

1. On the 10-pin DIP switch block, the address of the module and the termination can be set.

PINs 1-6: Set the address of the module using binary

Bus Termination ON: PIN 9 and 10 UP

Bus Termination OFF: PIN 9 and 10 DOWN

2. On the 4-pin DIP switch block, the UART of the module can be set.

UART 1: Pins 1 and 2 UP (Pins 3 and 4 DOWN)

UART 2: Pins 3 and 4 UP (Pins 1 and 2 DOWN)

Configuration 1



Configuration 2



## Old Base Configuration 2

1. On the 6-pin DIP switch block, the UART can be set (1 or 2) and the bus termination.

UART 1: PIN 1 and 2 Up (PIN 3 and 4 Down)

UART 2: PIN 3 and 4 Up (PIN 1 and 2 Down)

Bus Termination ON: PIN 5 and 6 Up

Bus Termination OFF: PIN 5 and 6 Down

On the 8-pin DIP switch block, the address for the module can be set using binary. If the address is not set using the DIP switches, the address must be set using the test.commander software.

**Contact us today if you have any further questions!**